D-FF:

<https://assets.nexperia.com/documents/data-sheet/74LVC1G74.pdf>

tpd, clk -> Q: tpd<4.1

tcd, clk -> Q: 1<tcd

tsu, D -> clk: 1.1<tsu

th, D -> clk: 1.0<th

JK-FF:

<https://www.onsemi.com/pub/Collateral/MC74HC112-D.PDF>

tpd, tpd<21

tsu, 17<tsu

th, 3<th

8 Input NOR/OR gate:

<https://www.digchip.com/datasheets/download_datasheet.php?id=594735&part-number=M74HC4078>

tpd, tpd<16

8 Input NAND gate:

<https://assets.nexperia.com/documents/data-sheet/74LVC30A.pdf>

tpd, tpd<6.3

NOT gate:

<http://www.ti.com/lit/ds/symlink/sn74lvc1g04.pdf?ts=1591457271671&ref_url=https://www.google.com/>

tpd, tpd<3.1

tcd, 0.7<tcd

2 input AND gate:

<http://www.ti.com/lit/ds/symlink/sn74lvc1g08-q1.pdf?ts=1591456946082&ref_url=https://www.google.com/>

tpd, tpd<4

tcd, 1<tcd

2 input OR gate:

<https://www.digchip.com/datasheets/download_datasheet.php?id=89921&part-number=74LX1G32CTR>

tpd, tpd<4.1

tcd, 1<tcd

2 Input XOR Gate

<https://www.onsemi.com/pub/Collateral/MC74LCX86-D.PDF>

tpd, tpd<6.5

4 input OR gate:

<https://www.digchip.com/datasheets/download_datasheet.php?id=594725&part-number=M74HC4072C1R>

tpd, tpd<17

3 Input AND Gate

<https://www.onsemi.com/pub/Collateral/MC74HC11A-D.PDF>

tpd, tpd<16

3 input OR gate

<https://assets.nexperia.com/documents/data-sheet/74LVC1G332.pdf>

tpd, tpd<3.5

2 Input NOR Gate

<https://www.ti.com/lit/ds/sces194n/sces194n.pdf?ts=1601137853467&ref_url=https%253A%252F%252Fwww.google.com%252F>

tpd, tpd<4.4

3 input NOR gate

<https://assets.nexperia.com/documents/data-sheet/74LVC1G27.pdf>

tpd, tpd<3.6

4 input AND gate

<https://assets.nexperia.com/documents/data-sheet/74HC21.pdf>

tpd, tpd<10

2 Input NAND Gate

<https://www.onsemi.com/pub/Collateral/MC74VHCT00A-D.PDF>

tpd, tpd<7.9

3-input XOR gate

<https://assets.nexperia.com/documents/data-sheet/74LVC1G386.pdf>

tpd, tpd<5.5

2 input multiplexer

<https://assets.nexperia.com/documents/data-sheet/74LVC1G157.pdf>

tpd, tpd<4

8-bit magnitude comparator

<https://assets.nexperia.com/documents/data-sheet/74HC688.pdf>

tpd, tpd<20

64Kx8 bit RAM

<https://www.digchip.com/datasheets/download_datasheet.php?id=479592&part-number=KM68512ALG>

tCYC, tCYC>55

tAA, tAA<55

tOH, tOH>10

tWHZ, 0<tWHZ<20

tDW, tDW>25

512x8 bit rom

<https://drive.google.com/file/d/0B9rh9tVI0J5mMDQyYzRjZDktMGRhZi00ZjM3LWIxYjYtNzU4MGU5NTFjZDQ0/view>

tAA, tAA<60

tOH, tOH<5

Bus Buffer

<https://www.onsemi.com/pub/Collateral/MC74VHCT126A-D.PDF>

tpd, tpd<5.5

tEN, tEN<5.1

tDS, tDS<8.8